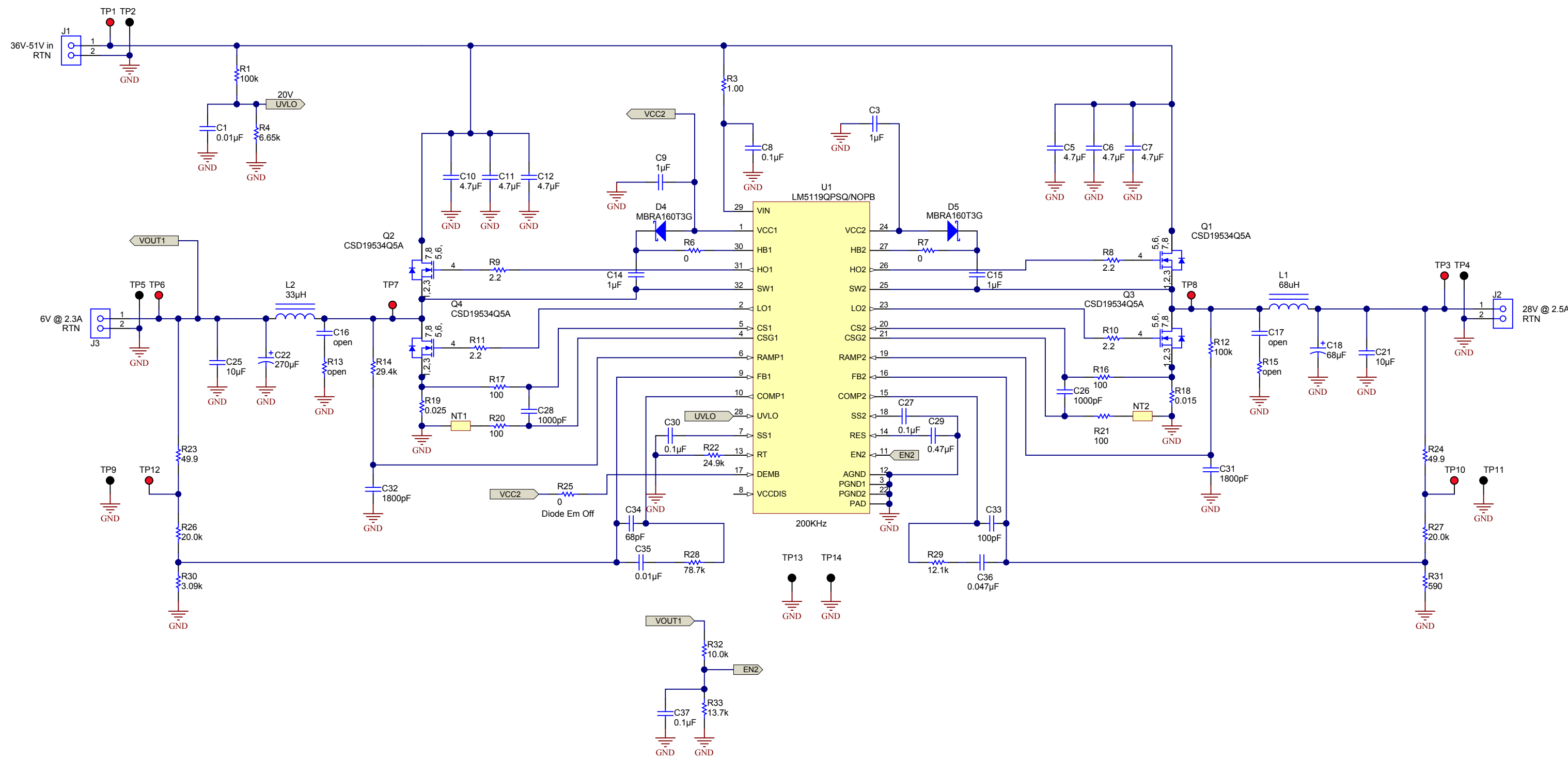


NOTES:
1) Preliminary design only.
2) Ref Des' match PMP9227 REVA PWB.



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

| | | |
|-----------------------------------|--|---|
| Orderable: N/A | Designed for: Public Release | Mod. Date: 5/21/2015 |
| TID #: | Project Title: LM5119 dual sync buck | |
| Number: PMP11098 | Rev: B | Sheet Title: |
| SVN Rev: Version control disabled | Assembly Variant: 001 | Sheet: 1 of 1 |
| Drawn By: | File: PMP11098 REVB.SchDoc | Size: B |
| Engineer: J Betten | Contact: http://www.ti.com/support | http://www.ti.com |
| © Texas Instruments 2015 | | |

